

Notice of Allowability

Application No.

09/900,277

Examiner

Kambiz Zand

Applicant(s)

HARTKE ET AL.

Art Unit

2132

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to reply filed on 06/27/2005 and the interview conducted on 07/08/2005.
2. ☒ The allowed claim(s) is/are 58-60, now re-numbered as claims 1-3.
3. ☒ The drawings filed on 27 June 2005 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

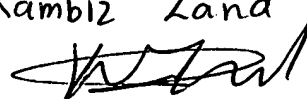
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413),
Paper No./Mail Date 07/08/05 enclosed.
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

Kambiz Zand


DETAILED ACTION
EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Robert B. O'Rourke on 07/08/2005.

The application has been amended as follows:

Claim 58

(Currently amended) A computing system, comprising:

a central processing unit;

a host memory coupled to said central processing unit, said host memory [to store]storing instructions executed by said central processing unit and data operated upon by way of said central processing unit executing said instructions;

a network interface [to receive]receiving inbound IP packets and sending outbound IP packets;

an offload processing subsystem, said offload processing subsystem communicatively coupled to said central processing unit and said network interface, said offload processing subsystem comprising:

Art Unit: 2132

- a) TCP/IP logic circuitry [for] processing TCP/IP tasks on said inbound and outbound IP packets without using said central processing unit and said host memory;
- b) SSL decryption logic circuitry [for] processing SSL tasks on said inbound IP packets without using said central processing unit and said host memory;
- c) SSL encryption logic circuitry [for] processing SSL tasks on said outbound IP packets without using said central processing unit and said host memory;
- d) a storage resource comprising one or more memory chips coupled to both said SSL decryption logic circuitry and said SSL encryption logic circuitry, said storage resource [to store]storing SSL processing information;
- e) an offload memory coupled to said TCP/IP logic circuitry, said offload memory [to store]storing said inbound and outbound TCP/IP packets, said network interface coupled to said offload memory;
- f) a first direct memory access (DMA) controller [to retrieve]retrieving said inbound IP packets from said offload memory, said first DMA controller coupled to said SSL decryption logic circuitry;
- g) a second direct memory access (DMA) controller [to retrieve]retrieving said outbound IP packets from said host memory, said second DMA controller coupled to said SSL encryption logic circuitry[;].

Claim 59

(Currently amended) [An]The computing system as in claim 58 wherein said inbound network interface comprises an Ethernet interface.

2. The text of those sections of Title 35, U.S. Code not included in this section can be found in the prior office action.
3. The prior office actions are incorporated herein by reference. In particular, the observations with respect to claim language, and response to previously presented arguments.
4. Examiner withdraws objections to the drawings due to corrections by Applicant.
5. Claims 58 and 59 have been amended by Examiner amendment.
6. Claims 58-60, now re-numbering as claims 1-3 are pending.

Response to Arguments

7. Applicant's arguments filed 07/27/2005 and the agreement reached on the interview dated 07/08/2005 (see enclosed interview summary) have been fully considered and they are persuasive.

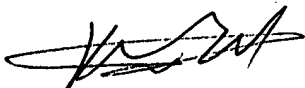
Allowable Subject Matter

8. Claims 58-60 are allowed.

Conclusion

9. Any comments considered necessary by the applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submission should be clearly labeled "comments on statement of reasons for allowance."

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kambiz Zand whose telephone number is (571) 272-3811. The examiner can normally be reached on Monday-Thursday (8:00-5:00). If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gilberto Barron can be reached on (571) 272-3799. The fax phone numbers for the organization where this application or proceeding is assigned is (703) 872-9306. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Kambiz Zand

07/08/2005

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fig 1-5b
approved
07/08/05
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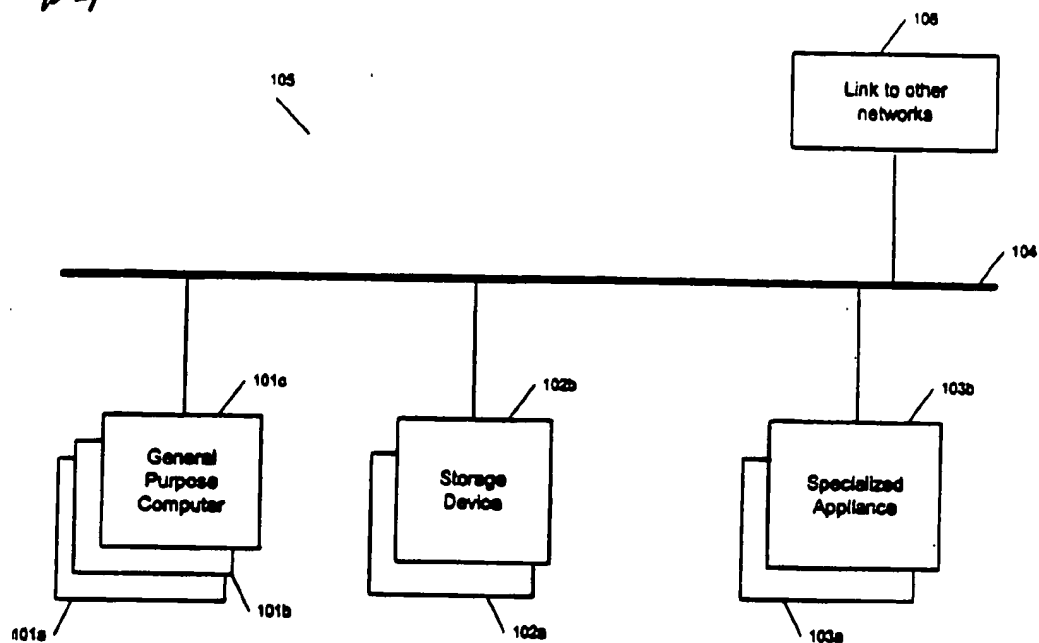


Figure 1. Local Area Network

(PRIOR ART)